

FIG. 1

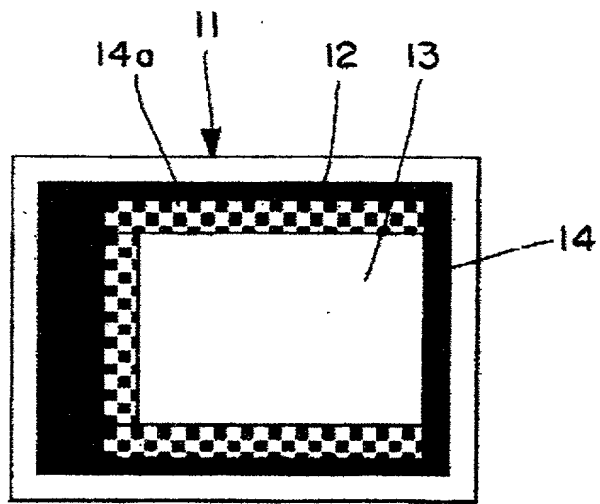


FIG. 2a

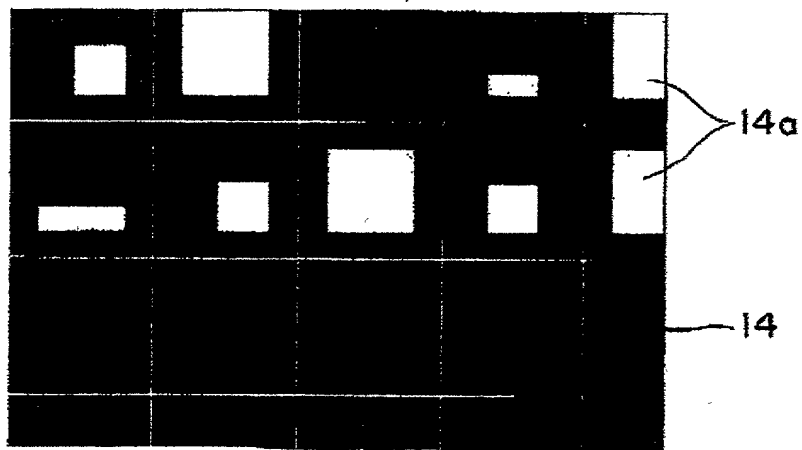


FIG. 2b

VERTICAL AND
HORIZONTAL
SYNCHRONIZING
SIGNALS

OUTPUT WAVEFORM
EXCLUDING
CHARACTERISTIC
DATA

OUTPUT WAVEFORM
INCLUDING
CHARACTERISTIC
DATA

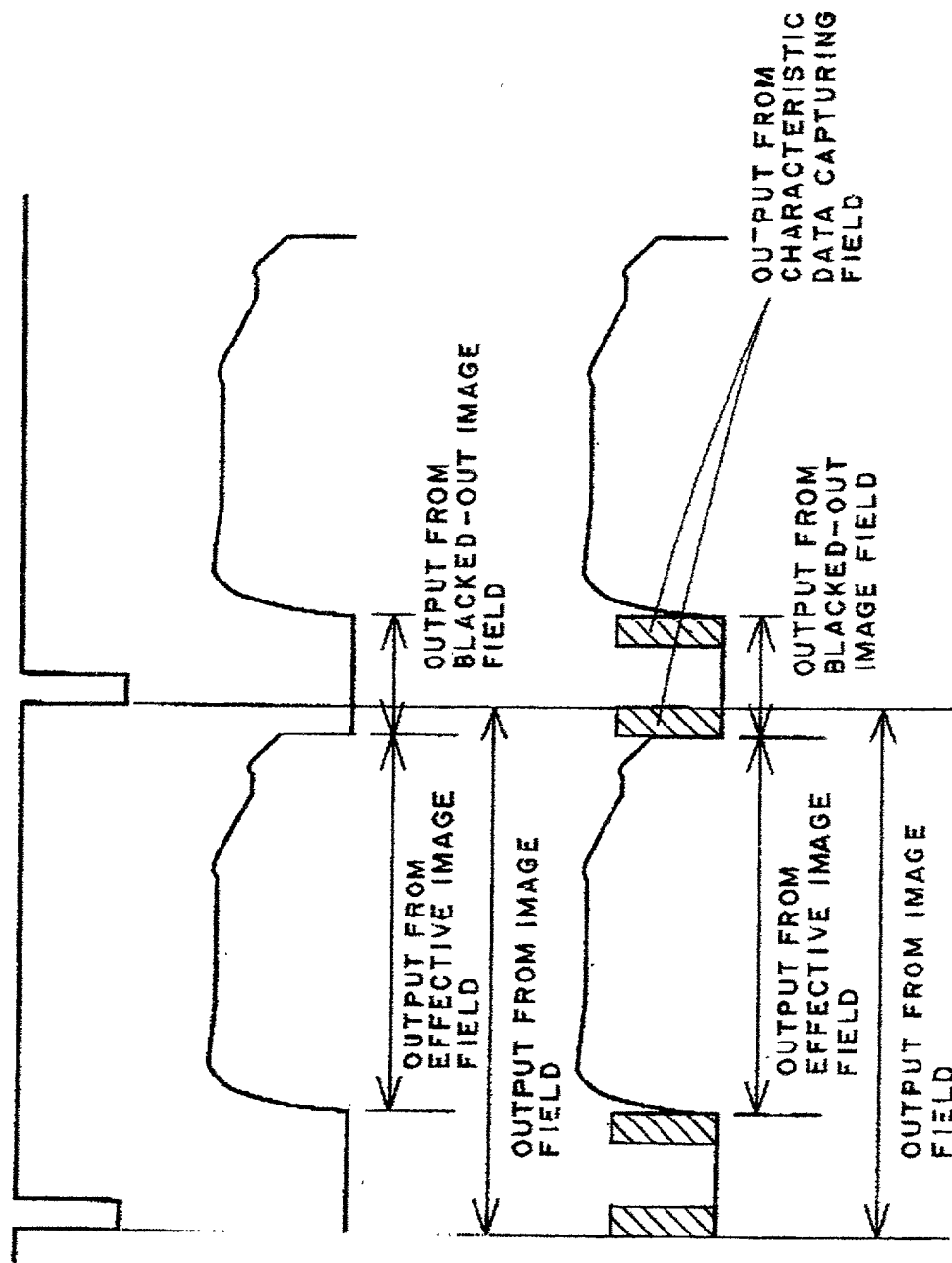


FIG. 3

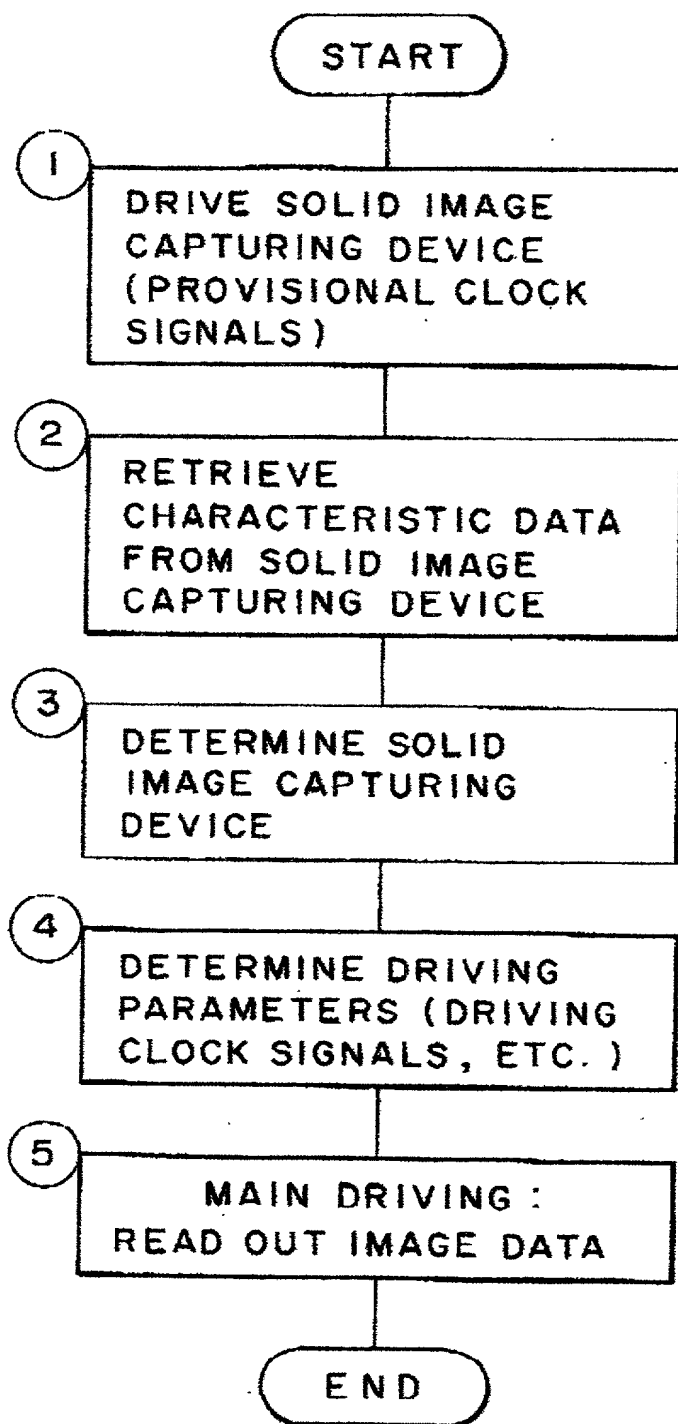


FIG. 4

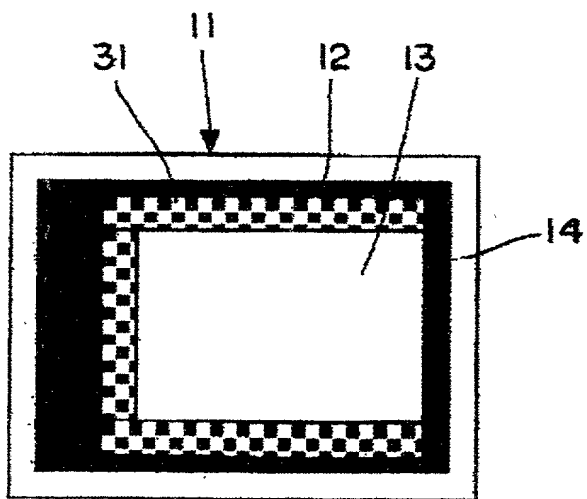


FIG. 5

FIG. 6 is a block diagram of a system for processing an input signal. The system includes an input device 24, a lens 22, a CDS circuit 25, an A/D conversion circuit 26, a memory 27, a controller 29, and a T/G circuit 28. The input device 24 is connected to the lens 22, which is connected to the CDS circuit 25. The CDS circuit 25 is connected to the A/D conversion circuit 26, which is connected to the memory 27. The controller 29 is connected to the A/D conversion circuit 26 and the memory 27. The T/G circuit 28 is connected to the A/D conversion circuit 26 and the controller 29.

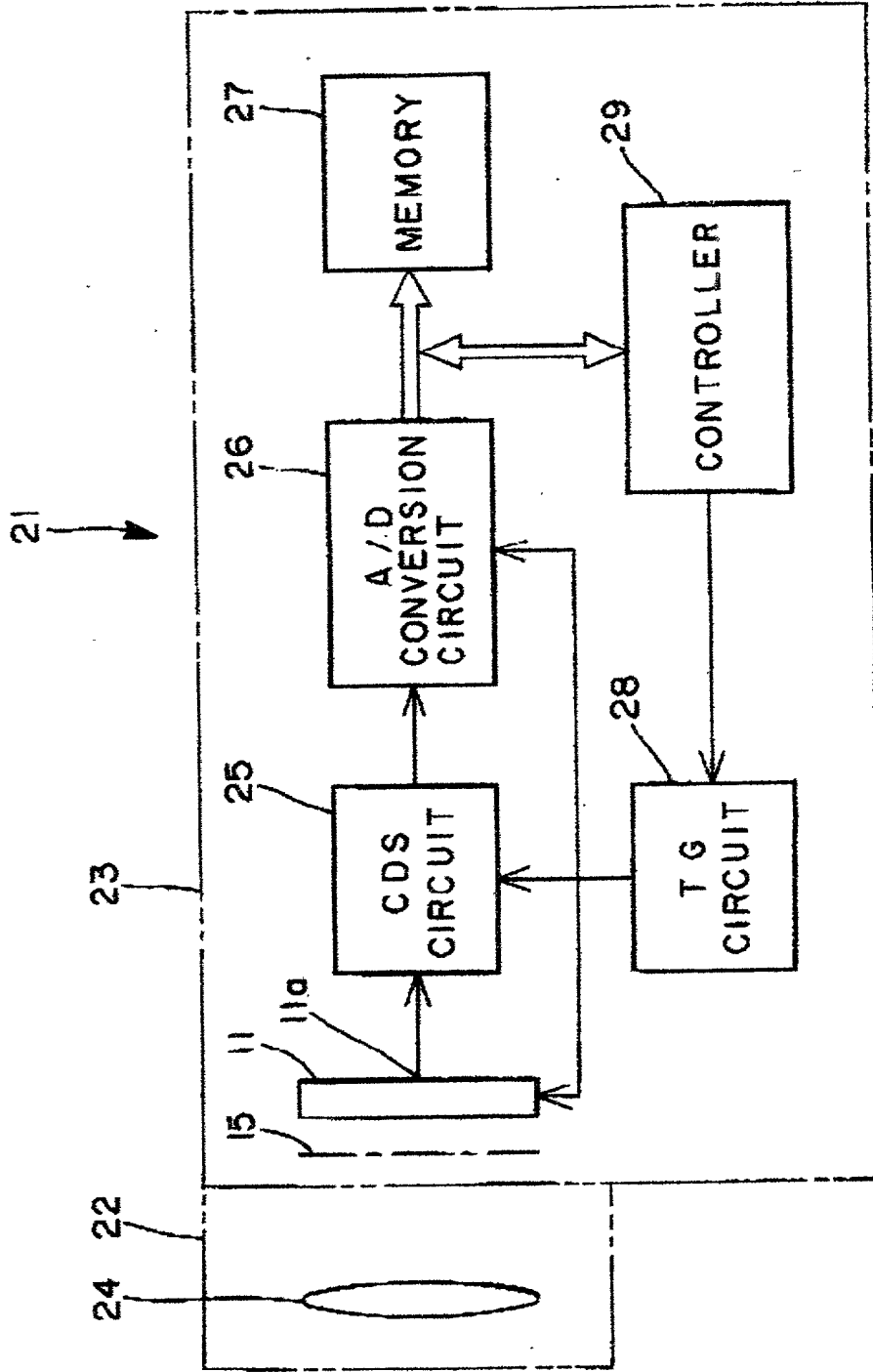


FIG. 6

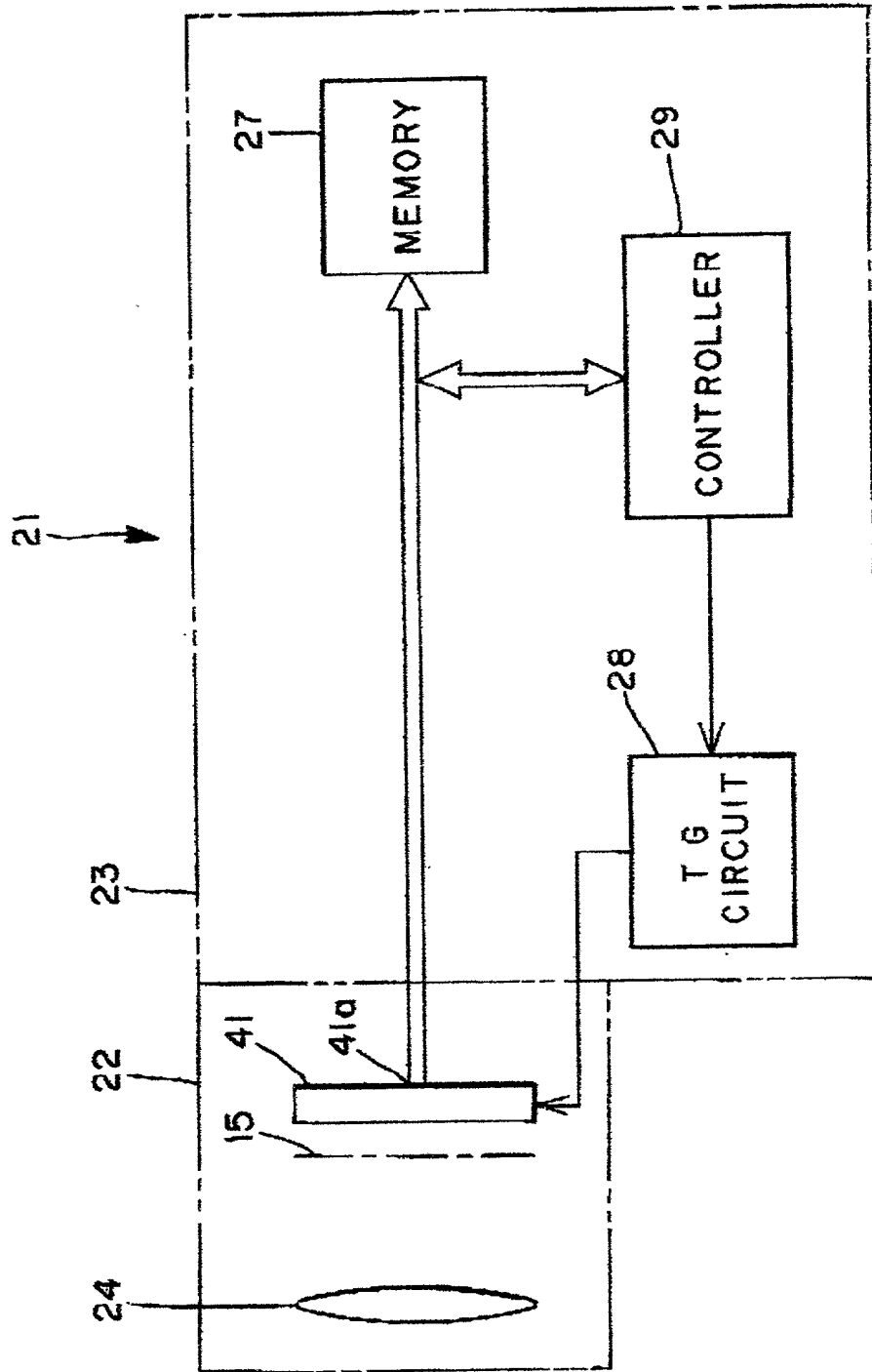


FIG. 7

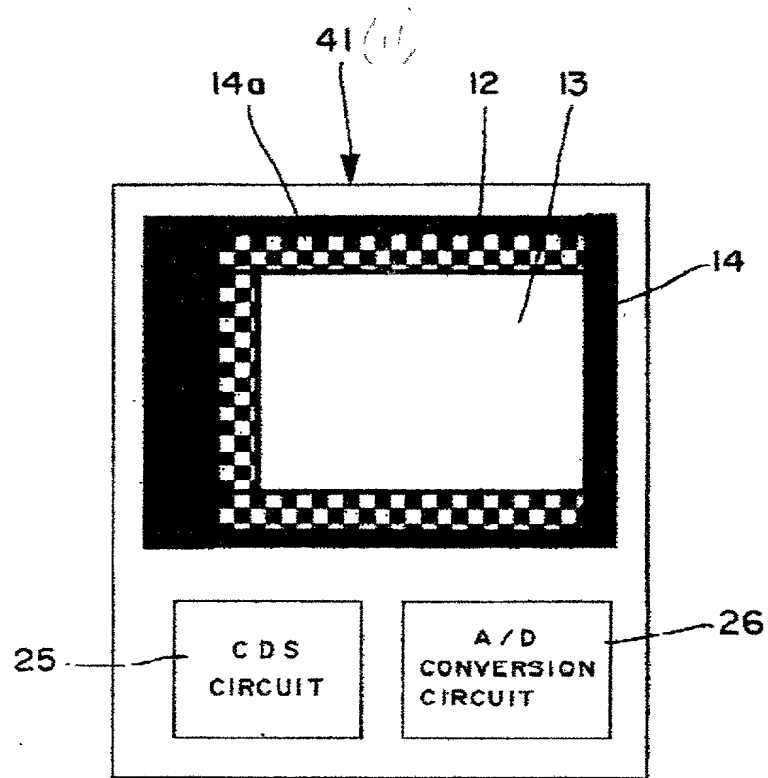


FIG. 8

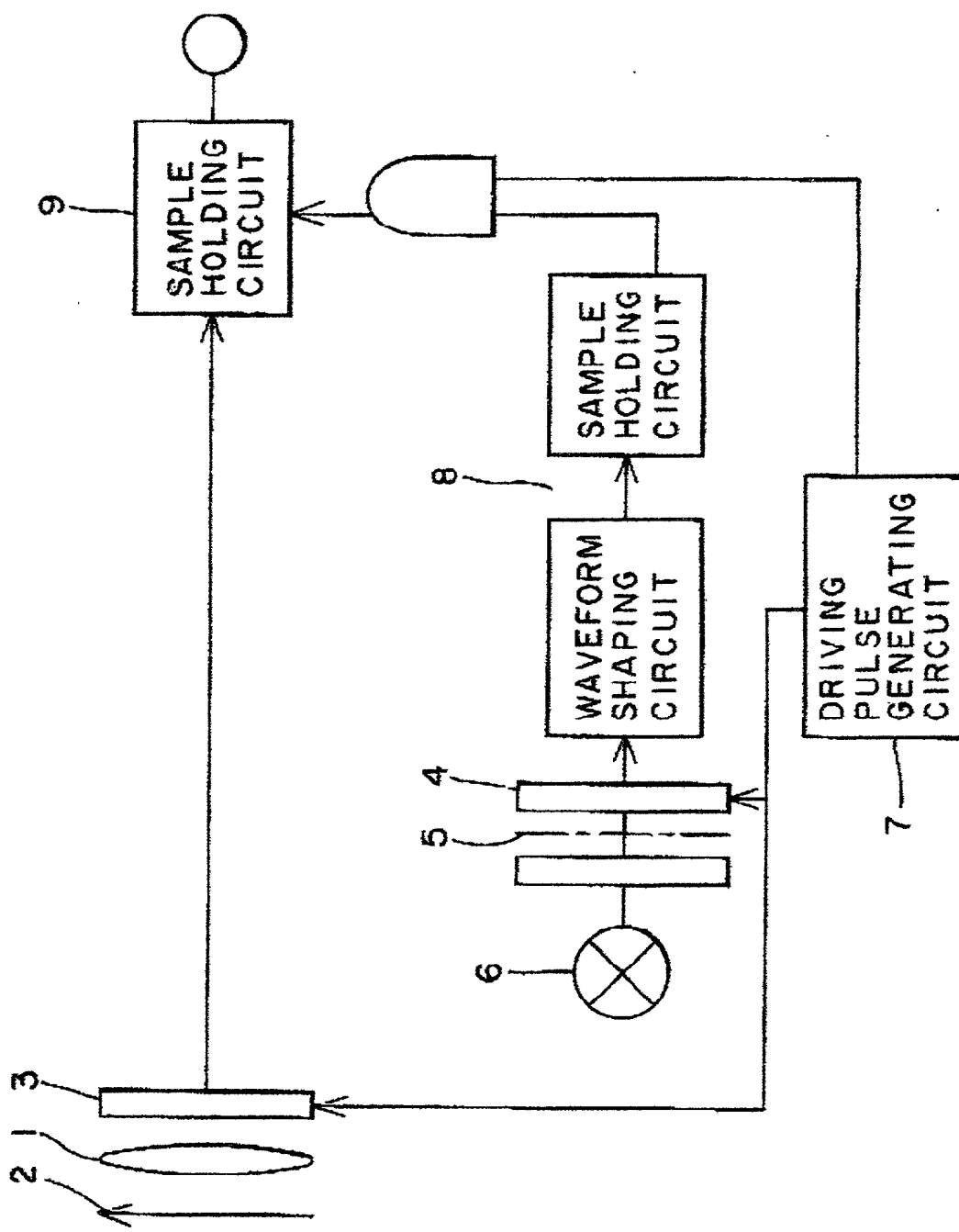


FIG. 9

WAVEFORM
REPRESENTING
OUTPUT FROM CCD



FIG. 10a

OUTPUT FROM CCD
MEMORY



FIG. 10b